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SEP 16 2005

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Chambers Art Unit: 2829  
Serial No.: 10/649,425 Examiner: Sarkar, A.  
Filing Date: 08/27/2003 Docket No.: TI-35714  
Customer No.: 23494 Conf. No.: 5169  
Title: STRUCTURE AND METHOD TO FABRICATE SELF-ALIGNED  
TRANSISTORS WITH DUAL WORK FUNCTION METAL GATE ELECTRODES

REQUEST FOR EXTENSION OF TIME

Commissioner of Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

## CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by  
facsimile to the U.S. Patent and Trademark Office at 571-273-8300 on  
the date shown below:

  
Jacqueline J. Garner, Reg. No. 36,144September 16, 2005  
Date

Pursuant to 37 CFR 1.136(a), Applicant(s) respectfully petitions the Commissioner  
for an extension of the shortened statutory period for response in the above-identified  
Application.

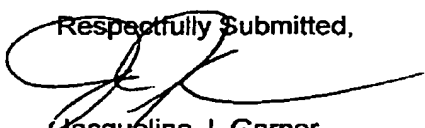
The fee for this extension is indicated below:

- ☒ One Month (\$120)  
☐ Two Months (\$450)  
☐ Three Months (\$1,020)  
☐ Four Months (\$1,590)

Please charge the fee to deposit account no. 20-0668. Any further necessary  
extension of time is hereby requested. Charge any and all fees to deposit account no. 20-  
0668.

Texas Instruments Incorporated  
PO Box 655474, M/S 3999  
Dallas, TX 75265  
(214) 532-9348  
Fax: (972) 917-4418

Respectfully Submitted,

  
Jacqueline J. Garner  
Reg. No. 36,144

09/19/2005 EAREGAY1 00000043 200668 10649425

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